

**U.S. UTILITY Patent Application**

O.I.P.E.	PATENT DATE
AC SCANNED <i>IM</i> <sup>3</sup> Q.A. <i>VR</i>	

APPLICATION NO. 09/655595	CONT/PRIOR	CLASS 257 703	SUBCLASS 28	ART UNIT 2811 2123	EXAMINER Stevens, Tom
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William Beausoleil  
R. Cook  
Tak-Kwong Ng  
Helmut Roth

High-speed software driven emulator comprised of a plurality of emulation processors with improved board-to-board interconnection cable length identification system

PTO-2040  
12/99

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	

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